

Distributed by:



TFT DISPLAY SPECIFICATION



WINSTAR Display Co.,Ltd.
華凌光電股份有限公司



Winstar Display Co., LTD
華凌光電股份有限公司



WEB: <https://www.winstar.com.tw> E-mail: sales@winstar.com.tw

SPECIFICATION

CUSTOMER : _____

MODULE NO.: **WF1560ATWAA5LNN0#**

APPROVED BY: (FOR CUSTOMER USE ONLY)	PCB VERSION: _____ DATA: _____
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SALES BY	APPROVED BY	CHECKED BY	PREPARED BY
			葉虹蘭
ISSUED DATE: 2025/01/14			

Precaution in use of TFT module: <https://www.winstar.com.tw/technology/download/declaration.html>



RECORDS OF REVISION

DOC. FIRST ISSUE

VERSION	DATE	REVISED PAGE NO.	SUMMARY
0	2024/02/20		First issue
A	2025/01/14		Modify Reliability



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1.Module Classification Information

W F 1560 A T W A A5 L N N 0 #
 ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨ ⑩ ⑪ ⑫ ⑬

①	Brand : WINSTAR DISPLAY CORPORATION											
②	Display Type : F→TFT Type, J→Custom TFT											
③	Display Size : 15.6” TFT											
④	Model serials no.											
⑤	Backlight Type :	F→CCFL, White S→LED, High Light White					T→LED, White Z→Nichia LED, White					
⑥	LCD Polarize Type/ Temperature range/ Gray Scale Inversion Direction	A→Transmissive, N.T, IPS TFT C→Transmissive, N. T, 6:00 ; F→Transmissive, N.T,12:00 ; I→Transmissive, W. T, 6:00 K→Transflective, W.T,12:00 L→Transmissive, W.T,12:00 N→Transmissive, Super W.T, 6:00					Q→Transmissive, Super W.T, 12:00 R→Transmissive, Super W.T, O-TFT V→Transmissive, Super W.T, VA TFT W→Transmissive, Super W.T, IPS TFT X→Transmissive, W.T, VA TFT Y→Transmissive, W.T, IPS TFT Z→Transmissive, W.T, O-TFT					
⑦	A : TFT LCD B : TFT+SCREW HOLES+CONTROL BOARD C : TFT+ SCREW HOLES +A/D BOARD D : TFT+ SCREW HOLES +A/D BOARD+CONTROL BOARD E : TFT+ SCREW HOLES +POWER BOARD					F : TFT+CONTROL BOARD G : TFT+ SCREW HOLES H : TFT+D/V BOARD I : TFT+ SCREW HOLES +D/V BOARD J : TFT+POWER BD						
⑧	Resolution:											
	M	1024768	N	128128	P	1280800	Q	480800	R	640320	S	480128
	T	800320	U	8001280	V	176220	W	1280398	X	1024250	Y	1920720
	A5	19201080	A6	480480	A7	10801920	A8	135240	A9	480640	B2	122250
	B3	340800	B4	2801424	B5	12001920	B6	4801280	B7	800800	B8	40160
⑨	D: Digital		L:LVDS		M:MIPI		E:eDP					
⑩	Interface:											
	N	Without control board			A	8Bit	B	16Bit	E	eDP	H	HDMI
	I	I2C Interface			R	RS232	S	SPI Interface	U	USB		
⑪	TS:											
	N	Without TS			T	Resistive touch panel			C	Capacitive touch panel (G-F-F)		
	G	Capacitive touch panel (G-G)					C1	Capacitive touch panel (G-F-F)+OCA				
	C2	Capacitive touch panel (G-F-F)+OCR					G1	Capacitive touch panel (G-G)+OCA				
	G2	Capacitive touch panel (G-G)+OCR					B	CTP+GG+USB				
⑫	Version:	X:Raspberry pi			V: Raspberry pi 3B+							
⑬	Special Code		#:Fit in with ROHS directive regulations									

2.Summary

WF1560A is a 15.6" TFT Liquid Crystal Display module with WLED Backlight unit and 40 pins 2ch-LVDS interface. This module supports 1920 x 1080 FHD AAS mode and can display 16.7M colors.



3.General Specifications

Item	Dimension	Unit
Size	15.6	inch
Dot Matrix	1920 x RGB x 1080	dots
Module dimension	363.8(W) x 215.9(H)x 9.3 (D)	mm
Active area	344.16 x 193.59	mm
Pixel pitch	0.17925 (H) x 0.17925 (V)	mm
LCD type	TFT, Normally Black, Transmissive	
Viewing Angle	89/89/89/89	
Backlight Type	LED, Normally White	
TFT Interface	2ch-LVDS	
With /Without TP	Without TP	
Surface	Anti-Glare	

*Color tone slight changed by temperature and driving voltage.

4. Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-30	—	+85	°C
Storage Temperature	TST	-40	—	+90	°C

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above

1. Temp. $\leq 60^{\circ}\text{C}$, 90% RH MAX. Temp. $> 60^{\circ}\text{C}$, Absolute humidity shall be less than 90% RH at 60°C



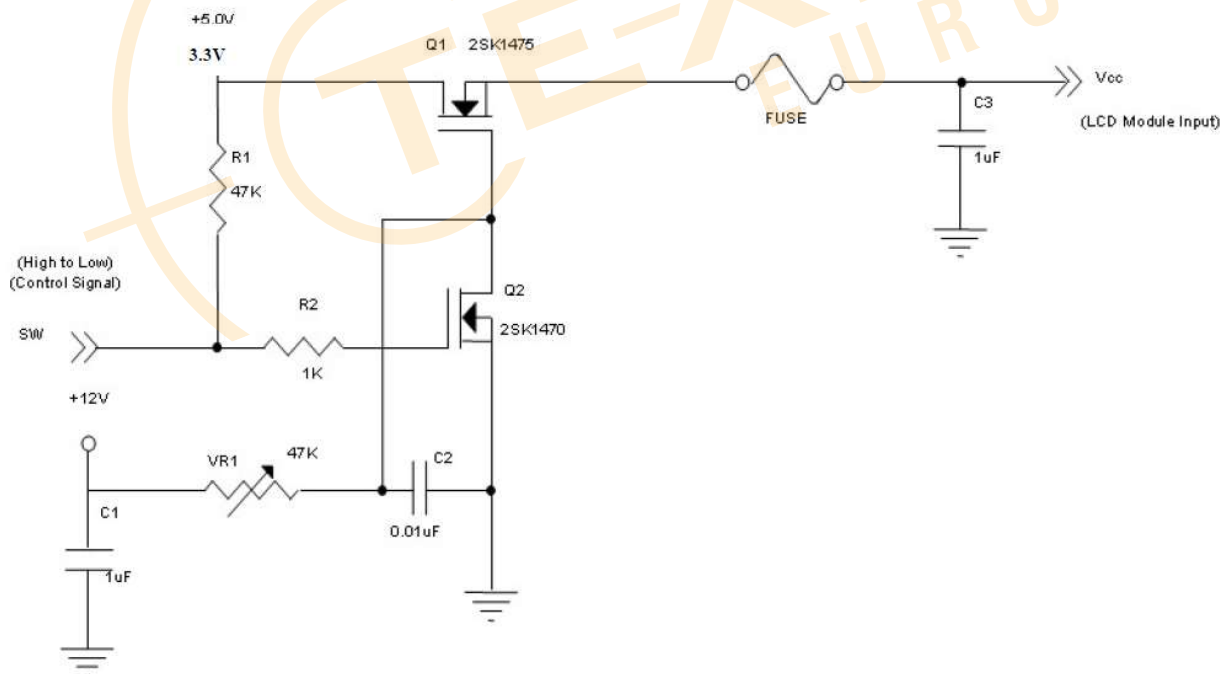
5. Electrical Characteristics

5.1. Operating conditions:

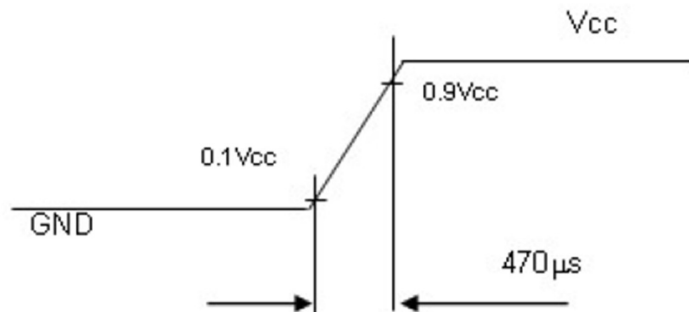
Parameter		Symbol	Value			Unit	Note
			Min.	Typ.	Max.		
Power Supply Voltage		V _{CC}	3.15	3.3	3.6	V	-
Ripple Voltage		V _{RP}	-	-	150	mV	-
Rush Current		I _{RUSH}	-	-	3	A	(2)
Power Supply Current	White	-	-	1.22	1.5	A	(3)a
	Black	-	-	0.51	0.7	A	(3)b
	Vertical Stripe	-	-	0.82	1	A	(3)c
Power Consumption		PLCD	-	4	5	Watt	(4)
LVDS differential input voltage		V _{id}	200		600	mV	(5)
LVDS common input voltage		V _{ic}	1.0	1.2	1.4	V	(5)
Differential Input Voltage for LVDS Receiver Threshold	"H" Level	V _{IH}	-	-	100	mV	-
	"L" Level	V _{IL}	-100	-	-	mV	-
LVDS terminating resistor		R _T		100		ohm	-

Note (1) The ambient temperature is $T_a = 25 \pm 2 \text{ }^{\circ}\text{C}$.

Note (2) Measurement Conditions:



Vcc rising time is 470μs



Note (3) The specified power supply current is under the conditions at $V_{cc} = 3.3\text{ V}$, $T_a = 25 \pm 2\text{ }^{\circ}\text{C}$, $F_r = 60\text{ Hz}$, whereas a power dissipation check pattern below is displayed.

a. White Pattern



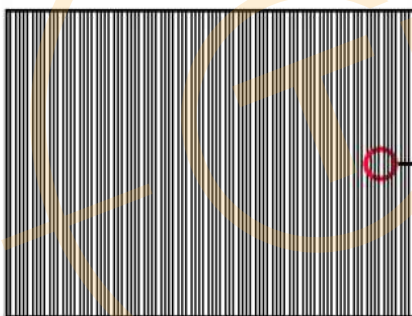
Active Area

b. Black Pattern



Active Area

c. Vertical Stripe Pattern

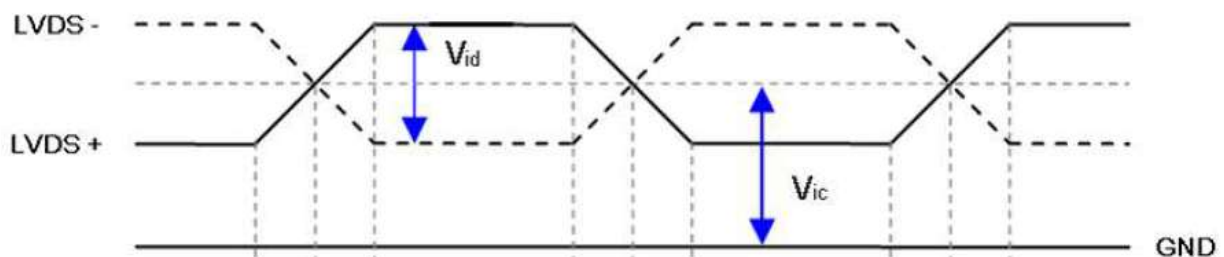


Active Area



Note (4) The power consumption is specified at the pattern with the maximum current.

Note (5) VID waveform condition

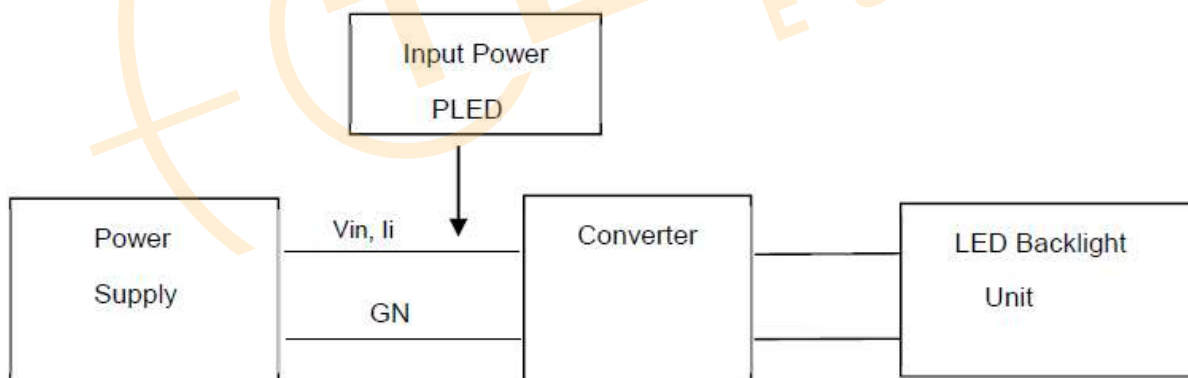


5.2. Backlight conditions:

Parameter		Symbol	Value			Unit	Note
			Min.	Typ.	Max.		
Converter Power Supply Voltage		LED_Vin	10.8	12.0	13.2	V	
Converter Power Supply Current		Ii	0.8	1.0	1.2	A	@LED_Vin= 12V Duty=100%
Converter Input Rush Current		Iirsh			3	A	@LED_Vin rising = 1mS
Power Consumption		P _{LED}		12		W	@ LED_Vin = 12V Duty=100%
EN Control Level	Backlight on	LED_EN	2.0	5	5.5	V	
	Backlight off		0	0	0.15		
PWM Control Level	PWM High Level	LED_PWM	2.0	3.3	5.0	V	
	PWM Low Level		0	0	0.15		
PWM Control Duty Ratio			10	--	100	%	
PWM Control Frequency		f _{PWM}	190	200	20k	Hz	
LED Life Time		L _L	50,000			Hrs	(2)

Note (1) LED light bar input voltage and current are measured by utilizing a true RMS multimeter as shown below:

Note (2) The lifetime of LED is estimated data and defined as the time when it continues to operate under the conditions at Ta = 25±2°C and Duty 100% until the brightness becomes ≤ 50% of its original value. Operating LED under high temperature environment will reduce life time and lead to color shift.



6.LVDS Input Signal Specifications

6.1. LVDS DATA MAPPING TABLE

LVDS Channel O0	LVDS output	D7	D6	D4	D3	D2	D1	D0
	Data order	OG0	OR5	OR4	OR3	OR2	OR1	OR0
LVDS Channel O1	LVDS output	D18	D15	D14	D13	D12	D9	D8
	Data order	OB1	OB0	OG5	OG4	OG3	OG2	OG1
LVDS Channel O2	LVDS output	D26	D25	D24	D22	D21	D20	D19
	Data order	DE	NA	NA	OB5	OB4	OB3	OB2
LVDS Channel O3	LVDS output	D23	D17	D16	D11	D10	D5	D27
	Data order	NA	OB7	OB6	OG7	OG6	OR7	OR6
LVDS Channel E0	LVDS output	D7	D6	D4	D3	D2	D1	D0
	Data order	EG0	ER5	ER4	ER3	ER2	ER1	ER0
LVDS Channel E1	LVDS output	D18	D15	D14	D13	D12	D9	D8
	Data order	EB1	EB0	EG5	EG4	EG3	EG2	EG1
LVDS Channel E2	LVDS output	D26	D25	D24	D22	D21	D20	D19
	Data order	DE	NA	NA	EB5	EB4	EB3	EB2
LVDS Channel E3	LVDS output	D23	D17	D16	D11	D10	D5	D27
	Data order	NA	EB7	EB6	EG7	EG6	ER7	ER6



6.2. COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 8-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of color versus data input

Color		Data Signal																							
		Red								Green								Blue							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale Of Red	Red(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...
	Red(253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...
Gray Scale Of Green	Green(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...
	Green(253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0
	Green(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...
Gray Scale Of Blue	Blue(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...
	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...	...

Note (1) 0: Low Level Voltage, 1: High Level Voltage

7.Display Timing Specifications

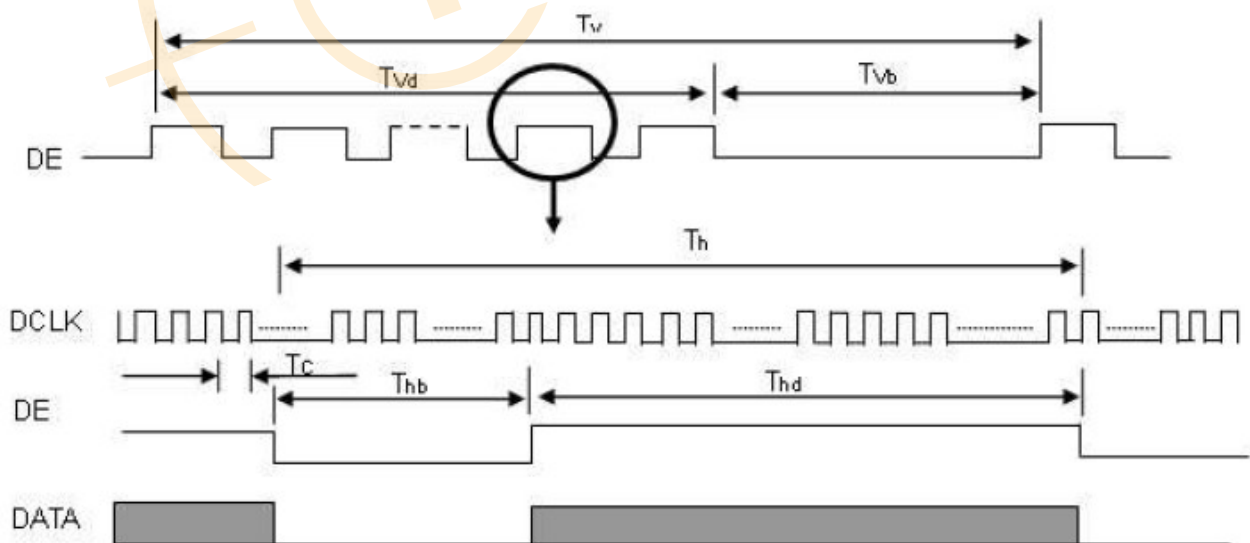
7.1. The input signal timing specifications are shown as the following table and timing diagram.

Signal	Item	Symbol	Min.	Typ.	Max.	Unit	Note
LVDS Clock	Frequency	F _c	60	70.93	73	MHz	-
	Period	T _c		14,1		ns	
	Input cycle to cycle jitter	T _{rel}	-0.02*T _c		0.02*T _c	ns	(3)
	Input clock to data skew	TLVCCS	-0.02*T _c		0.02*T _c	ns	(4)
	Spread spectrum modulation range	F _{clk_in_mod}	FC*98%		FC*102%	MHz	(5)
	Spread spectrum modulation frequency	F _{SSM}			200	KHz	
Vertical Display Term	Frame Rate	Fr	50	60	60	Hz	T _v =T _{vd} +T _{vb}
	Total	T _v	1090	1110	1130	Th	-
	Active Display	T _{vd}	1080	1080	1080	Th	-
	Blank	T _{vb}	T _v -T _{vd}	30	T _v -T _{vd}	Th	-
Horizontal Display Term	Total	T _h	1050	1065	1075	T _c	T _h =T _{hd} +T _{hb}
	Active Display	T _{hd}	960	960	960	T _c	-
	Blank	T _{hb}	T _h -T _{hd}	105	T _h -T _{hd}	T _c	-

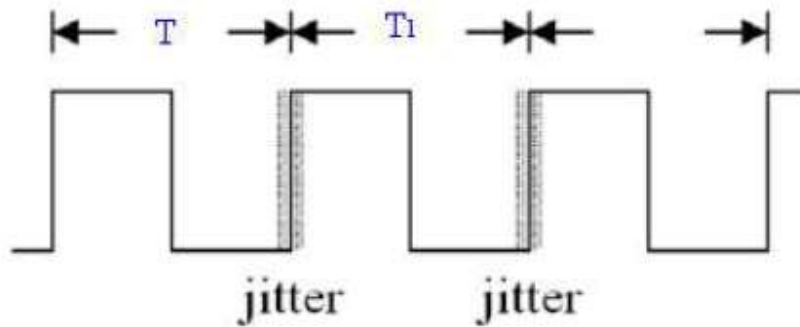
Note (1) Because this module is operated by DE only mode, Hsync and Vsync input signals are ignored.

Note (2) The T_v(T_{vd}+T_{vb}) must be integer, otherwise, this module would operate abnormally.

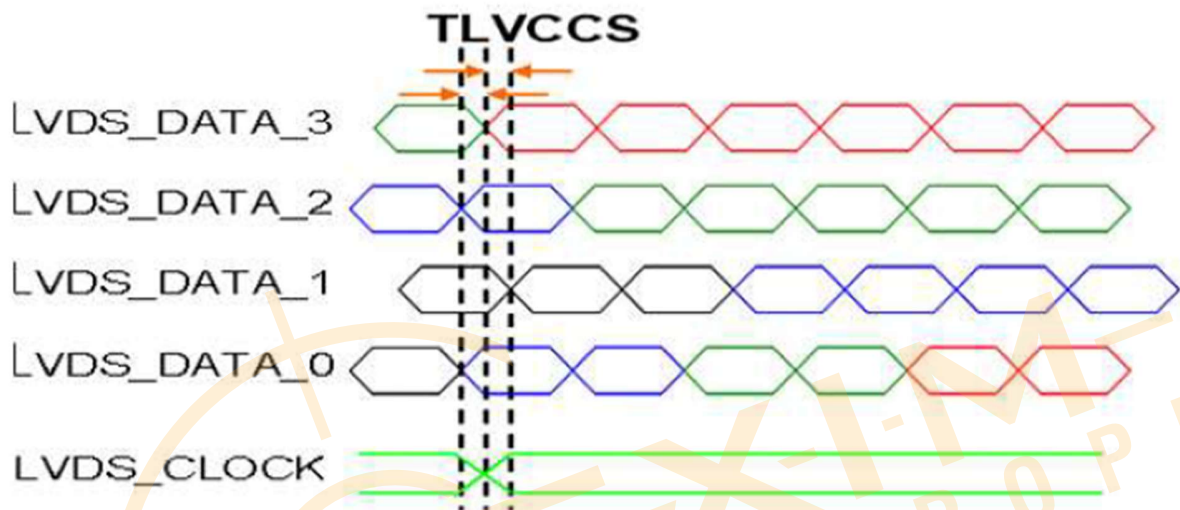
Input Signal Timing Diagram



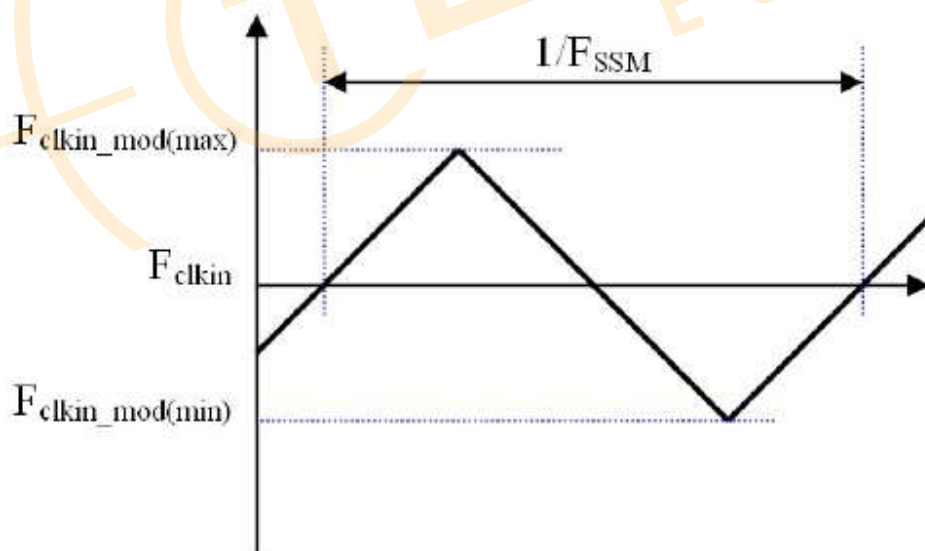
Note (3) The input clock cycle-to-cycle jitter is defined as below figures. $Trcl = |T1 - Tl|$



Note (4) Input Clock to data skew is defined as below figures

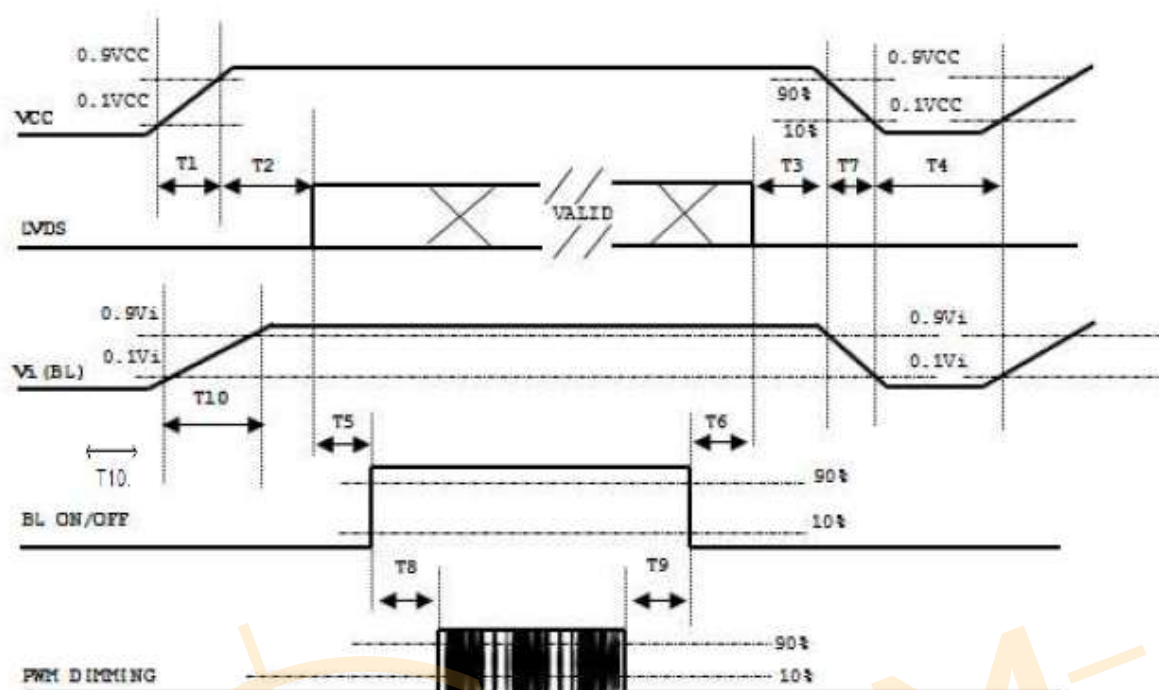


Note (5) The SSCG (Spread spectrum clock generator) is defined as below figures.



8.Power ON/OFF Sequence

The power sequence specifications are shown as the following table and diagram.



Timing Specifications:

Parameter	Value			Units
	Min	Typ	Max	
T1	0.5	-	10	ms
T2	0	-	50	ms
T3	0	-	50	ms
T4	500	-	-	ms
T5	450	-	-	ms
T6	200	-	-	ms
T7	10	-	100	ms
T8	10	-	-	ms
T9	10	-	-	ms
T10	20	-	50	ms

Note:

- 1.The supply voltage of the external system for the module input should be the same as the definition of Vcc.
- 2.When the backlight turns on before the LCD operation of the LCD turns off, the display may momentarily become abnormal screen.
- 3.In case of VCC = off level, please keep the level of input signals on the low or keep a high impedance.
- 4.T4 should be measured after the module has been fully discharged between power off and on period.
- 5.Interface signal shall not be kept at high impedance when the power is on.
- 6.Winstar won't take any responsibility for the products which are damaged by the customers not following the Power Sequence.
- 7.There might be slight electronic noise when LCD is turned off (even backlight unit is also off). To avoid this symptom, we suggest "Vcc falling timing" to follow "T7 spec"



9.Optical Characteristics

Item		Symbol	Condition.	Min	Typ.	Max.	Unit	Remark
Response time		Tr+ Tf	$\theta=0^{\circ}$ 、 $\Phi=0^{\circ}$	-	25	35	.ms	Note 3
Contrast ratio		CR	At optimized viewing angle	600	800	-	-	Note 4,
Color Chromaticity	White	Wx	$\theta=0^{\circ}$ 、 $\Phi=0$	0.263	0.313	0.363		Note 2,6,7
		Wy		0.279	0.329	0.379		
Viewing angle	Hor.	Θ R	$CR\geq 10$	85	89	-	Deg.	Note 1
		Θ L		85	89	-		
	Ver.	Φ T		85	89	-		
		Φ B		85	89	-		
Brightness		-	-	360	450	-	cd/m ²	Center of display
Uniformity		(U)	-	70	-	-	%	Note 5

Ta=25±2°C

Note 1: Definition of viewing angle range

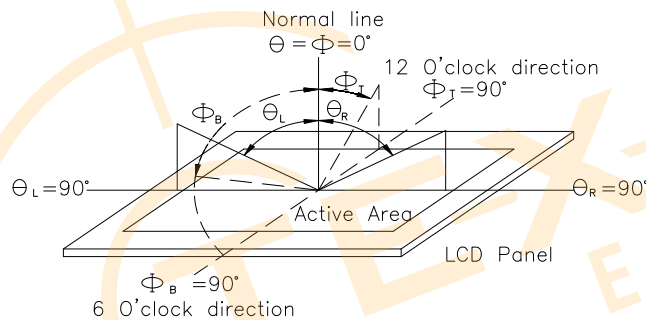


Fig 9.1. Definition of viewing angle

Note 2: Test equipment setup:

After stabilizing and leaving the panel alone at a driven temperature for 10 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7 or BM-5 luminance meter 1.0° field of view at a distance of 50cm and normal direction.

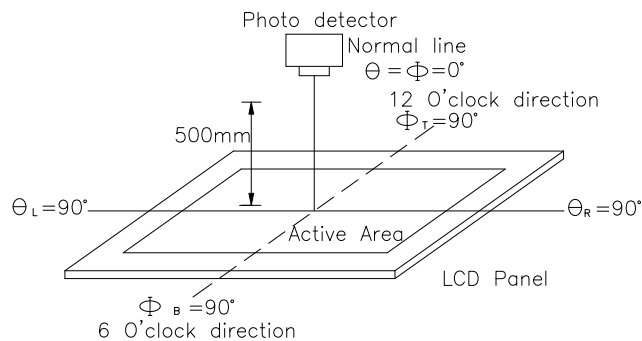
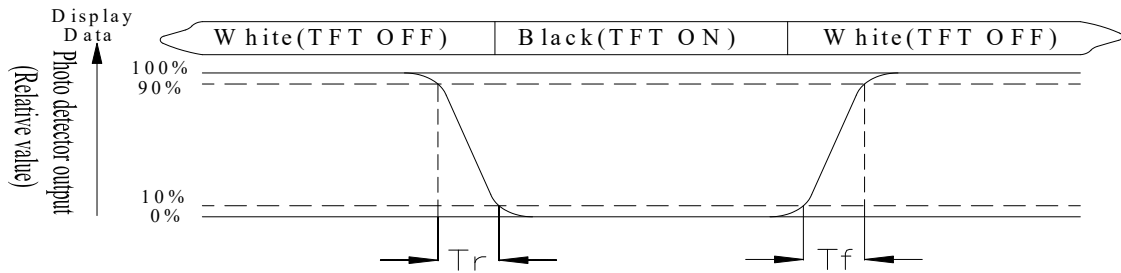


Fig 9.2. Optical measurement system setup

Note 3: Definition of Response time:

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time, Tr, is the time between photo detector output intensity changed

from 90% to 10%. And fall time, T_f , is the time between photo detector output intensity changed from 10% to 90%



Note 4: Definition of contrast ratio:

The contrast ratio is defined as the following expression.

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note 5: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (reference the picture in below). Every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity (U)} = L_{\min}/L_{\max} \times 100\%$$

L = Active area length

W = Active area width

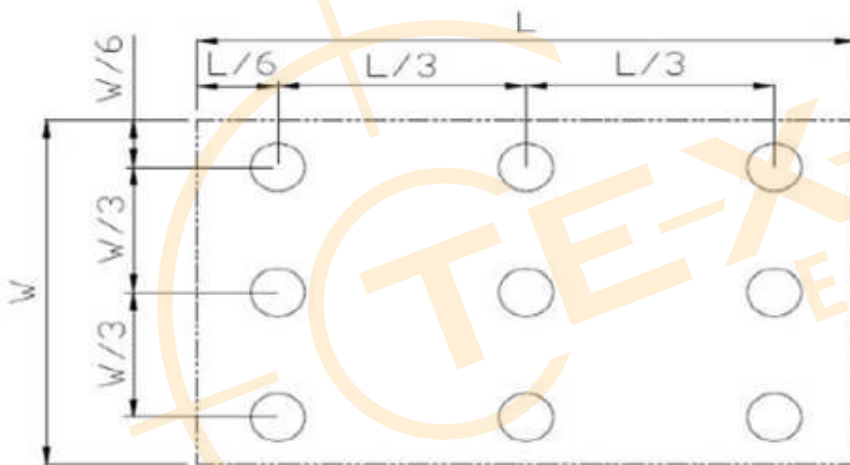


Fig 9.3. Definition of uniformity

Note 6: Definition of color chromaticity (CIE 1931)

Color coordinates measured at the center point of LCD

Note 7: Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

Interface

10.1. CON2 : LCM PIN Definition

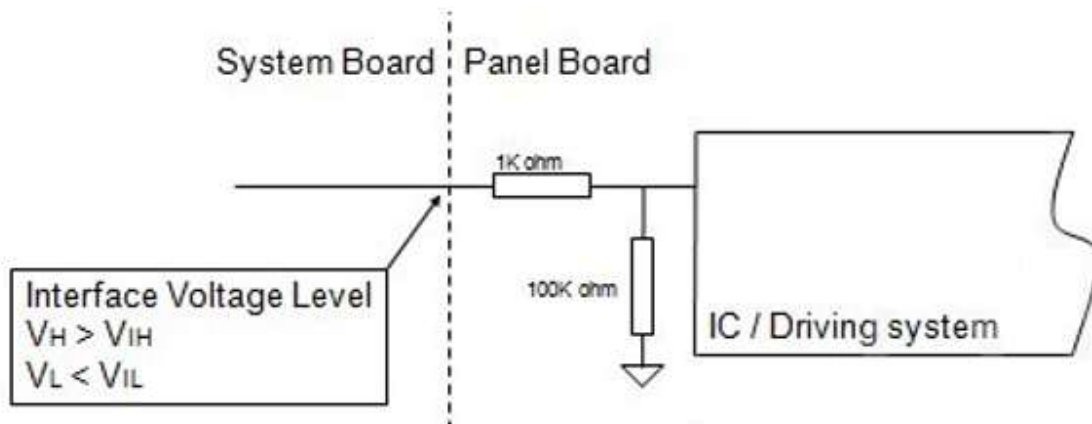
Pin	Name	Description	Note
1~4	LED_Vcc	+12V Vi power supply	-
5~8	GND	Ground	-
9	LED_EN	Enable pin	-
10	LED_PWM	Backlight Adjust	-
11~13	LCD_VCC	LCD logic and driver power 3.3V	-
14~16	NC	Not connection, this pin should be open	-
17	REV_SCAN	Low → Normal Mode. High → Horizontal & Vertical Reverse Scan	(3)
18	RXO0-	Negative LVDS differential data input. Channel O0 (odd)	-
19	RXO0+	Positive LVDS differential data input. Channel O0 (odd)	-
20	RXO1-	Negative LVDS differential data input. Channel O1 (odd)	-
21	RXO1+	Positive LVDS differential data input. Channel O1 (odd)	-
22	RXO2-	Negative LVDS differential data input. Channel O2 (odd)	-
23	RXO2+	Positive LVDS differential data input. Channel O2 (odd)	-
24	LCD GND	LCD logic and driver ground	-
25	RXOC-	Negative LVDS differential clock input. (odd)	-
26	RXOC+	Positive LVDS differential clock input. (odd)	-
27	LCD GND	LCD logic and driver ground	-
28	RXO3-	Negative LVDS differential data input. Channel O3(odd)	-
29	RXO3+	Positive LVDS differential data input. Channel O3 (odd)	-
30	RXE0-	Negative LVDS differential data input. Channel E0 (even)	-
31	RXE0+	Positive LVDS differential data input. Channel E0 (even)	-
32	RXE1-	Negative LVDS differential data input. Channel E1 (even)	-
33	RXE1+	Positive LVDS differential data input. Channel E1 (even)	-
34	LCD GND	LCD logic and driver ground	-
35	RXE2-	Negative LVDS differential data input. Channel E2 (even)	-
36	RXE2+	Positive LVDS differential data input. Channel E2 (even)	-
37	RXEC-	Negative LVDS differential clock input. (even)	-
38	RXEC+	Positive LVDS differential clock input. (even)	-
39	RXE3-	Negative LVDS differential data input. Channel E3 (even)	-
40	RXE3+	Positive LVDS differential data input. Channel E3 (even)	-

Note (1) Connector Part No.: I-PEX 20455-040E-76 or equivalent.

Note (2) User's connector Part No.: I-PEX 20453-040T-03 or equivalent.

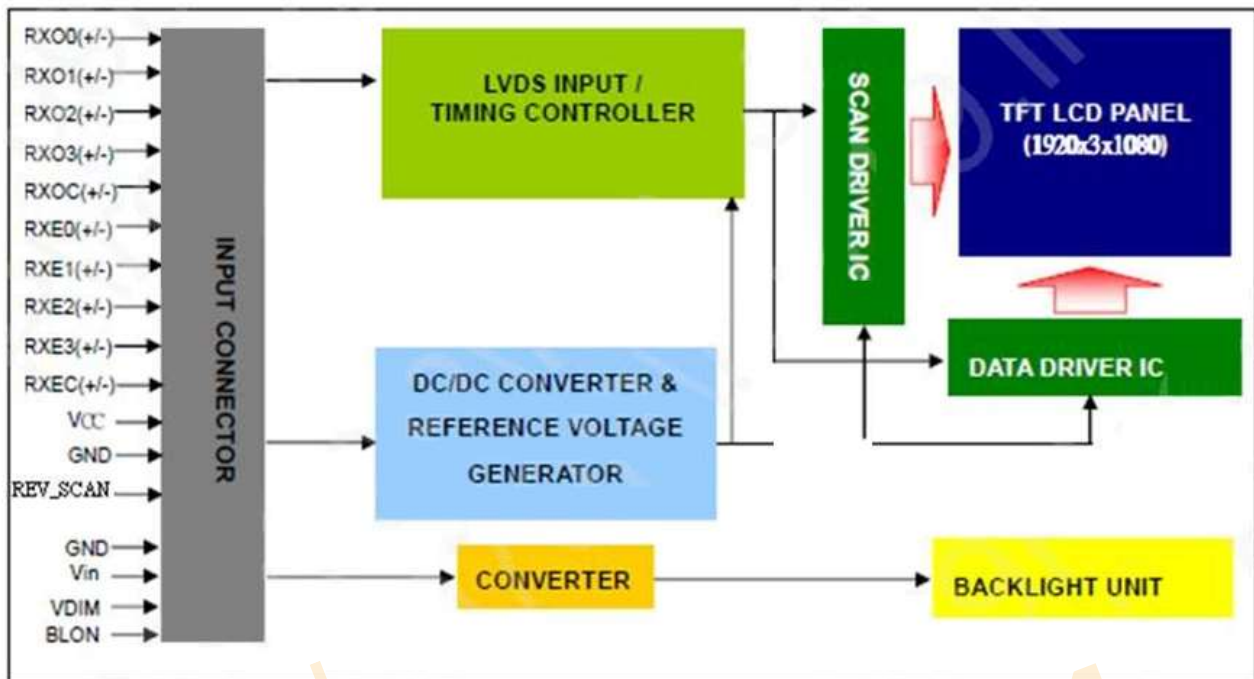
Note (3) "Low" stands for 0V. "High" stands for 3.3V.

REV_SCAN PIN :



TEXIM-
EUROPE

11. Block Diagram



12. Reliability

Content of Reliability Test (Super Wide temperature, -30°C~85°C)

Environmental Test			
Test Item	Content of Test	Test Condition	Note
High Temperature storage	Endurance test applying the high storage temperature for a long time.	90°C 200hrs	2
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-40°C 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	85°C 200hrs	2
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-30°C 200hrs	1,2
High Temperature/ Humidity Operation	The module should be allowed to stand at 60°C,90%RH max	60°C,90%RH 96hrs	1,2
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of storage	-30°C/85°C 10 cycles	2
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude : 1.5mm Vibration Frequency : 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=±6KV(contact), ±8KV(air), RS=330Ω CS=150pF 10 times	4

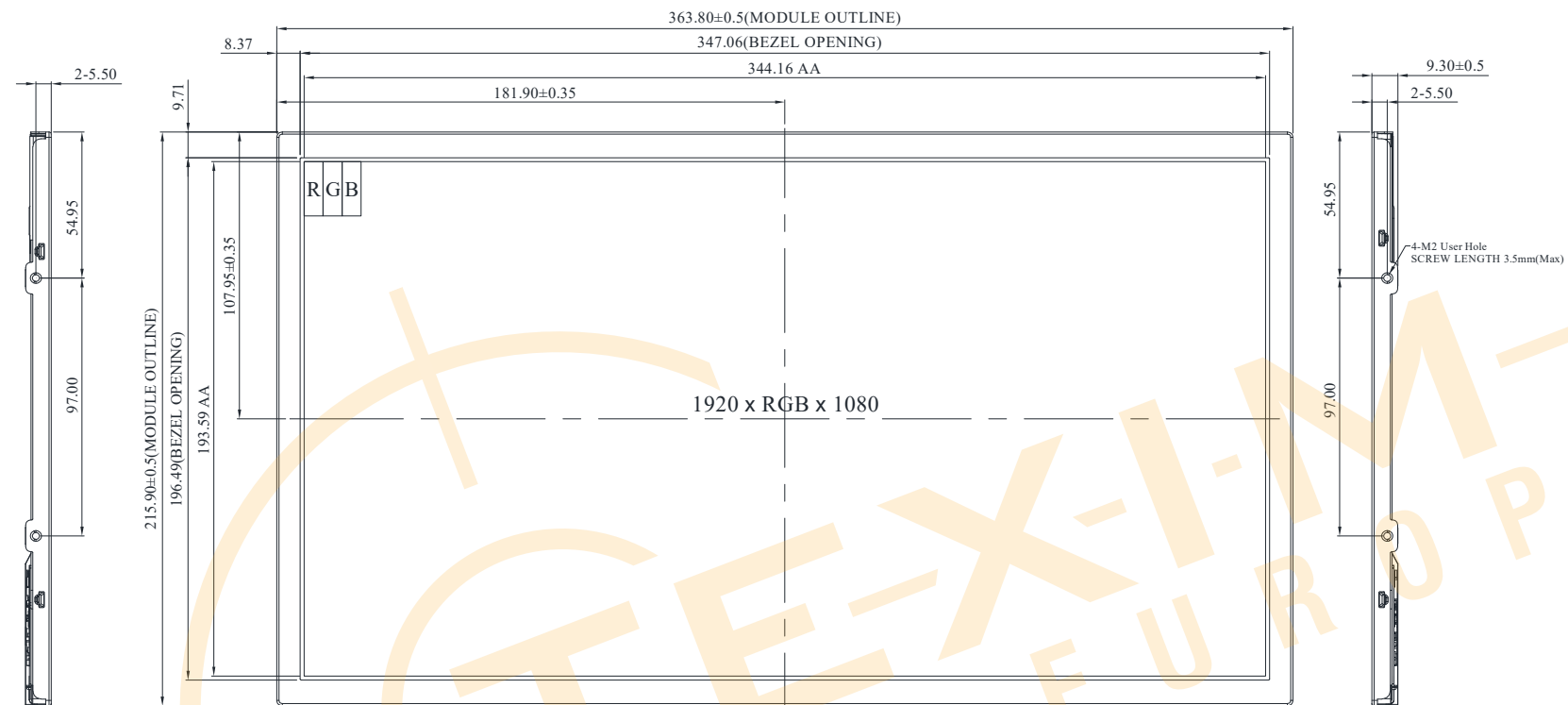
Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.

Note3: The packing have to including into the vibration testing.

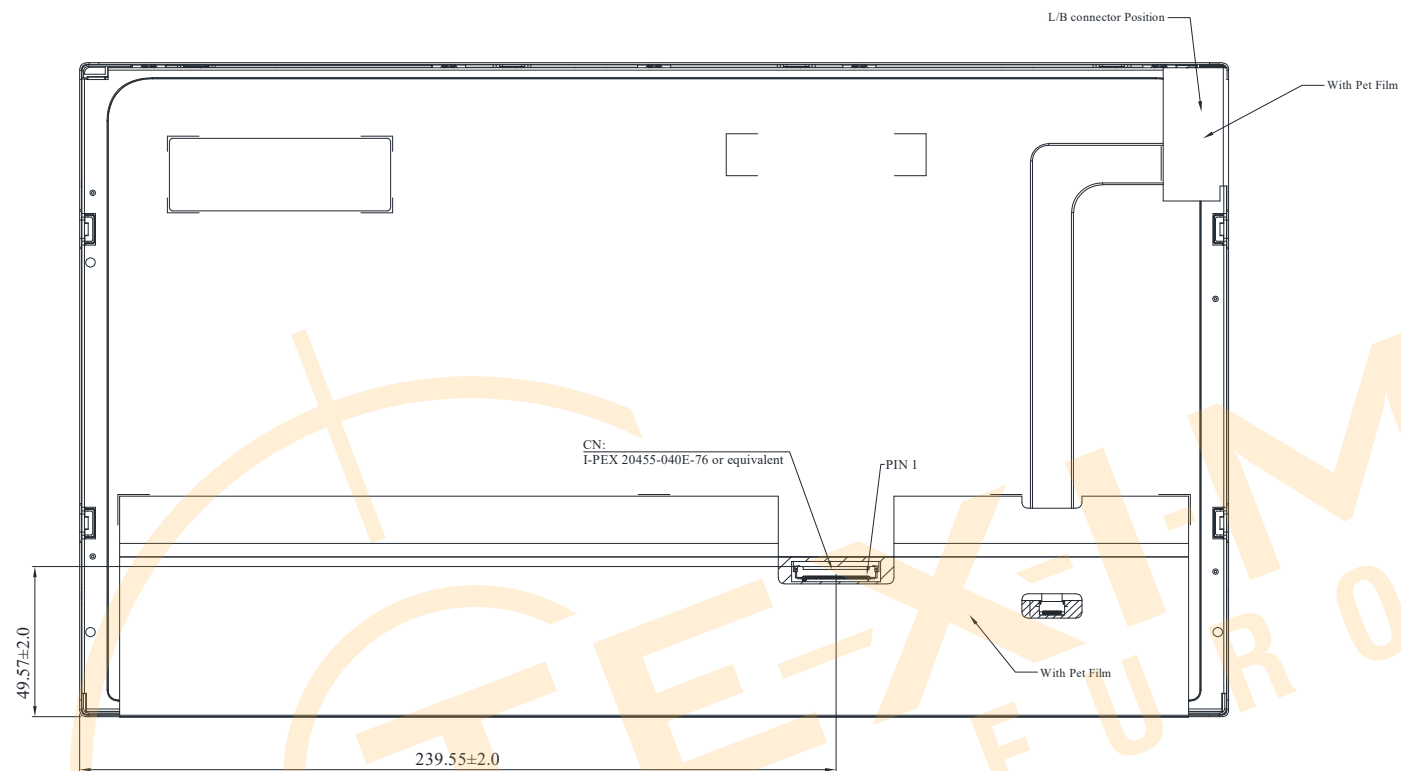
Note4: Endurance test applying the electric stress to the finished product housing

13. Contour Drawing



NOTE:
SIDE MOUNT HOLE ROTATIONAL TORQUE MAX. IS 6kgf-cm.

The non-specified tolerance of dimension is ± 0.3 mm .



CN

PinNo.	Symbol
1~4	LED_VCC
5~8	GND
9	LED_EN
10	LED_PWM
11~13	LCD_VCC
14~16	NC
17	REV_SCAN
18	RX00-
19	RX00+
20	RX01-
21	RX01+
22	RX02-
23	RX02+
24	LCD_GND
25	RX0C-
26	RX0C+
27	LCD_GND
28	RX03-
29	RX03+
30	RXE0-
31	RXE0+
32	RXE1-
33	RXE1+
34	LCD_GND
35	RXE2-
36	RXE2+
37	RXEC-
38	RXEC+
39	RXE3-
40	RXE3+

The non-specified tolerance of dimension is ± 0.3 mm .

14. Inspection Specification

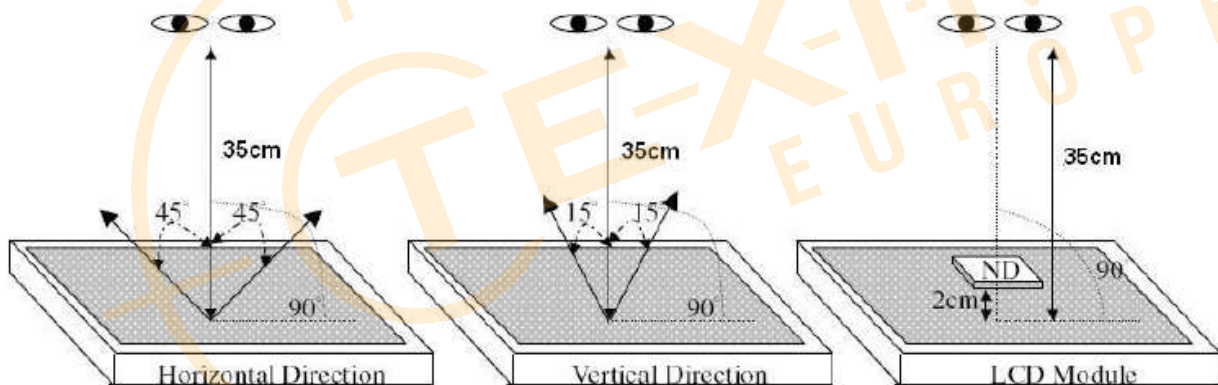
14.1. Description

These inspection standards shall be applied to LCD Module supplied by Winstar. This model is only used for Industry product. If there are any other product applications such as handwriting recognition, Medical use, Automobile, Aerospace use and so on, the specification should be negotiated separately.

14.2. The environmental condition of inspection

The environmental condition and visual inspection shall be conducted as below.

- (1) Ambient temperature : 15~25°C
- (2) Humidity : 25~75 %RH
- (3) External appearance inspection shall be conducted by using a single 20W fluorescent lamp or equivalent illumination.
- (4) Panel visual inspection on the operation condition for cosmetic shall be conducted at the distance 35cm or more between the LCD module and eyes of inspector.
Ambient Illumination : 300 ~ 500 Lux for external appearance inspection
Ambient Illumination : 100 ~ 200 Lux for light on inspection
- (5) The viewing angle :
 - a) 15 degree to the front surface of display panel in vertical direction.
 - b) 45 degree to the front surface of display panel in horizontal direction.
- (6) ND filter shall be conducted at the dist



14.3. Classification of defects

Defects are classified two types, major defect and minor defect according to the defect. And, the definition of defects is classified as below.

(1) Major defect

Any defect may result in functional failure, or reduce the usability of product for its purpose. For example, electrical failure, deformation and etc..

(2) Minor defect

A defect that is not to reduce the usability of product for its intended purpose and uniformity, dot defect and etc..

The criteria on major or minor judgment will be according with the classification of defects, and any defects out of active area, are not considered as a defect or counted.

14.4. Inspection Criteria

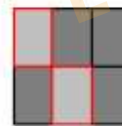
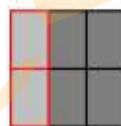
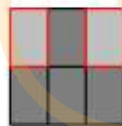
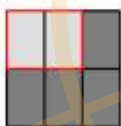
(1) Definition of dot defect induced from the panel inside

- a. Bright dot :Dots appear bright and unchanged in size in which module is displaying under black pattern.
- b. Dark dot :Dots appear dark and unchanged in size in which module is displaying under pure red,green, blue, white picture.
- c. 2 Full dot adjacent = 1pair.
- d. Picture :

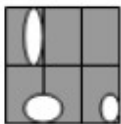
(a) Full dot



(b) 2 Full dot adjacent



(c) Spot defect



(2) Display Inspection standards when power on.

Items		Acceptable count
Full Bright dot	Random	$N \leq 2$
	2 dots adjacent	$N \leq 1$
	3 dots adjacent or more	$N \leq 0$
Full Dark dot	Random	$N \leq 3$
	2 dots adjacent	$N \leq 1$
	3 dots adjacent or more	$N \leq 0$
Total full bright and full dark dot		$N \leq 5$
Foreign Black/White/Bright Spot		$D \leq 0.15\text{mm}$, Ignore $0.15 < D \leq 0.5\text{ mm}$, $N \leq 4$
Foreign Black/White/Bright Line		$W \leq 0.05\text{mm}$, Ignore $0.05 < W \leq 0.1\text{ mm}$, $0.3 < L \leq 2.0\text{ mm}$, $N \leq 4$
Polarizer	Scratches	$W \leq 0.05\text{mm}$, Ignore $0.05 < W \leq 0.1\text{ mm}$, $0.3 < L \leq 2.0\text{ mm}$, $N \leq 4$
	Dent /Bubble	Avg. $0.15 < D \leq 0.5\text{ mm}$, $N \leq 4$
Distance	Minimum Distance Between Full Bright dots	$L \geq 10\text{mm}$
	Minimum Distance Between Full Dark dots	$L \geq 10\text{mm}$
Display failure (V-line/H-line/Cross line etc.)		Not allowable
Mura	Not visible through 6% ND filter in 50% gray or judge by limit sample if necessary	

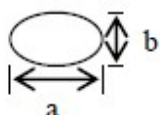
14.5. External Appearance Inspection Criteria (Power off)

Item	Contents	
Screw	Parts mounting, incomplete assembly, deformation, oxidized, crooked or rusty is not permitted.	
CCFL cable (For CCFL Model)	Cable not continuous 、 Break-off 、 Connector Burn-off /Break-off	
Metal frame (Bezel)	Scratch	*Noticeable scratch and exfoliation coating are not permitted. *The oxidized metal is not permitted.
	Incomplete assembly is not permitted.	
Backlight	Scratch	The scratch which may causes a problem in practical use is not permitted.
	Break-off	Breaking off is not permitted.
	Crack	The crack is not permitted.
Polarizer	Scratches	$W \leq 0.05\text{mm}$, Ignore $0.05 < W \leq 0.1 \text{ mm}$, $0.3 < L \leq 10.0 \text{ mm}$, $N \leq 4$
	Dent/Bubble	Avg. $0.15 < D \leq 0.5 \text{ mm}$, $N \leq 4$
	Stain	The stain on polarizer, which can't be wiped off, is not permitted.
Tape/Label	Incorrect position, missed label is not permitted.	
Connector	Oxidized/rusty connector is not permitted.	
Outline size	Spec. out is not permitted.	

*Note: If any specific defect is not included in the above defect table, this defect should be judged by INX/ODM/Brand customer discussion.

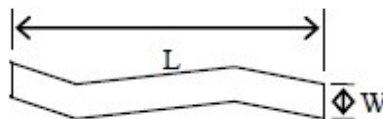
Note.1

$$D = (a+b)/2$$



Note.2

W: width, L : length



14.6. Classification of defects

Inspection Item	Criteria and Description	Defect type
Vertical line	Signal input, vertical line off or irregular V-line appears	major
Horizontal line	Signal input, horizontal line off or irregular H-line appears	major
Cross line	Pattern signal input, a correct display is not obtained	major
No display	Signal input, display is dead	major
Irregular display	Pattern signal input, a correct display is not obtained	major
Dots defect	Exceed specified standards	minor
Scratch and Dent on polarizer	Exceed specified standards	minor
Foreign material	Exceed specified standards	minor
Mura	Non-uniformity is appeared in display	minor
Polarizer bubble	Exceed specified standards	minor





winstar

LCM Sample Estimate Feedback Sheet

Module Number : _____

Page: 1

1、Panel Specification :

- | | | |
|----------------------------|-------------------------------|-------------------------------------|
| 1. Panel Type : | ☐ Pass | ☐ NG , _____ |
| 2. View Direction : | ☐ Pass | ☐ NG , _____ |
| 3. Numbers of Dots : | ☐ Pass | ☐ NG , _____ |
| 4. View Area : | ☐ Pass | ☐ NG , _____ |
| 5. Active Area : | ☐ Pass | ☐ NG , _____ |
| 6. Operating Temperature : | ☐ Pass | ☐ NG , _____ |
| 7. Storage Temperature : | ☐ Pass | ☐ NG , _____ |
| 8. Others : | _____ | |

2、Mechanical

- | | | |
|-----------------------------|-------------------------------|-------------------------------------|
| 1. PCB Size : | ☐ Pass | ☐ NG , _____ |
| 2. Frame Size : | ☐ Pass | ☐ NG , _____ |
| 3. Material of Frame : | ☐ Pass | ☐ NG , _____ |
| 4. Connector Position : | ☐ Pass | ☐ NG , _____ |
| 5. Fix Hole Position : | ☐ Pass | ☐ NG , _____ |
| 6. Backlight Position : | ☐ Pass | ☐ NG , _____ |
| 7. Thickness of PCB : | ☐ Pass | ☐ NG , _____ |
| 8. Height of Frame to PCB : | ☐ Pass | ☐ NG , _____ |
| 9. Height of Module : | ☐ Pass | ☐ NG , _____ |
| 10. Others : | ☐ Pass | ☐ NG , _____ |

3、Relative Hole Size :

- | | | |
|-----------------------------|-------------------------------|-------------------------------------|
| 1. Pitch of Connector : | ☐ Pass | ☐ NG , _____ |
| 2. Hole size of Connector : | ☐ Pass | ☐ NG , _____ |
| 3. Mounting Hole size : | ☐ Pass | ☐ NG , _____ |
| 4. Mounting Hole Type : | ☐ Pass | ☐ NG , _____ |
| 5. Others : | ☐ Pass | ☐ NG , _____ |

4、Backlight Specification :

- | | | |
|--|-------------------------------|-------------------------------------|
| 1. B/L Type : | ☐ Pass | ☐ NG , _____ |
| 2. B/L Color : | ☐ Pass | ☐ NG , _____ |
| 3. B/L Driving Voltage (Reference for LED) : | ☐ Pass | ☐ NG , _____ |
| 4. B/L Driving Current : | ☐ Pass | ☐ NG , _____ |
| 5. Brightness of B/L : | ☐ Pass | ☐ NG , _____ |
| 6. B/L Solder Method : | ☐ Pass | ☐ NG , _____ |
| 7. Others : | ☐ Pass | ☐ NG , _____ |

>> Go to page 2 <<



Winstar Module Number : _____

Page: 2

5、Electronic Characteristics of Module :

- | | | |
|------------------------------|-------------------------------|-------------------------------------|
| 1. Input Voltage : | ☐ Pass | ☐ NG , _____ |
| 2. Supply Current : | ☐ Pass | ☐ NG , _____ |
| 3. Driving Voltage for LCD : | ☐ Pass | ☐ NG , _____ |
| 4. Contrast for LCD : | ☐ Pass | ☐ NG , _____ |
| 5. B/L Driving Method : | ☐ Pass | ☐ NG , _____ |
| 6. Negative Voltage Output : | ☐ Pass | ☐ NG , _____ |
| 7. Interface Function : | ☐ Pass | ☐ NG , _____ |
| 8. LCD Uniformity : | ☐ Pass | ☐ NG , _____ |
| 9. ESD test : | ☐ Pass | ☐ NG , _____ |
| 10. Others : | ☐ Pass | ☐ NG , _____ |

6、Summary :

Sales signature : _____

Customer Signature : _____

Date : / /

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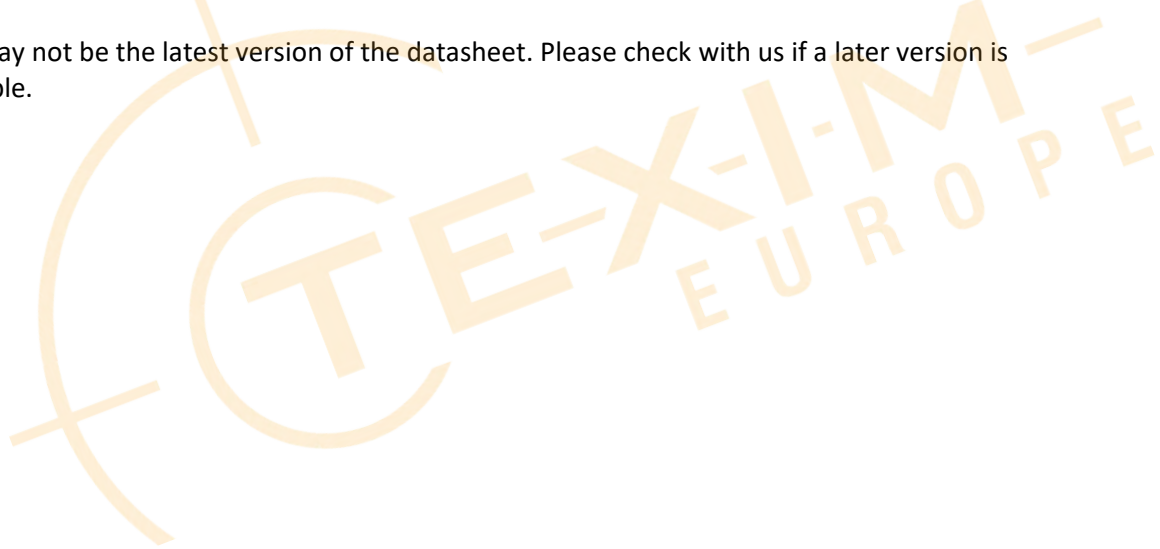
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All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts.

Please contact us if you have any questions about the contents of the datasheet.

This may not be the latest version of the datasheet. Please check with us if a later version is available.





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